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**UNITED STATES  
SECURITIES AND EXCHANGE COMMISSION**  
Washington, D.C. 20549

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**FORM 6-K**

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**REPORT OF FOREIGN PRIVATE ISSUER  
PURSUANT TO RULE 13a-16 OR 15d-16  
UNDER THE SECURITIES EXCHANGE ACT OF 1934**

For the month of October 2022

Commission File Number: 001-41426

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**Nano Labs Ltd**

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**30th Floor, Dikaiyinzuo  
No. 29, East Jiefang Road,  
Hangzhou, Zhejiang  
People's Republic of China**  
(Address of principal executive offices)

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Indicate by check mark whether the registrant files or will file annual reports under cover of Form 20-F or Form 40-F:

Form 20-F  Form 40-F

Indicate by check mark if the registrant is submitting the Form 6-K in paper as permitted by Regulation S-T Rule 101(b)(1):

Indicate by check mark if the registrant is submitting the Form 6-K in paper as permitted by Regulation S-T Rule 101(b)(7):

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**SIGNATURES**

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned, thereunto duly authorized.

**Nano Labs Ltd**

Date: October 14, 2022

By: /s/ Jianping Kong

Name: Jianping Kong

Title: Chairman and Chief Executive Officer

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**EXHIBIT INDEX**

<u>Exhibit No.</u>	<u>Description</u>
Exhibit 99.1	Press Release

**Nano Labs Regains Compliance with Nasdaq Listing Rule**

HANGZHOU, China, October 14, 2022 (GLOBE NEWSWIRE) — Nano Labs Ltd (the “Company” or “Nano Labs”), a leading fabless integrated circuit design company and product solution provider in China, announced today that it has received a letter from the Nasdaq Office of General Counsel, notifying the Company that it has regained compliance with Listing Rule 5210(k)(i). Accordingly, the Nasdaq Hearing Panel has determined to continue the listing of the Company’s securities on The Nasdaq Stock Market.

**About Nano Labs Ltd**

Nano Labs Ltd is a leading fabless integrated circuit (“IC”) design company and product solution provider in China. Nano Labs is committed to the development of high throughput computing (“HTC”) chips, high performance computing (“HPC”) chips, distributed computing and storage solutions, smart network interface cards (“NICs”) vision computing chips and distributed rendering. Nano Labs has built a comprehensive flow processing unit (“FPU”) architecture which offers solution that integrates the features of both HTC and HPC. Nano Lab’s Cuckoo series are one of the first near-memory HTC chips available in the market with a maximum bandwidth of approximately 2.27 Tbps, as well as one of the first movers of ASIC-based Grin mining market\*. For more information, please visit the Company’s website at: [ir.nano.cn](http://ir.nano.cn) .

\* According to an industry report prepared by Frost & Sullivan.

**For investor and media inquiries, please contact:****Nano Labs Ltd**

Email: [ir@nano.cn](mailto:ir@nano.cn)

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