UNITED STATES
SECURITIES AND EXCHANGE COMMISSION
WASHINGTON, D.C. 20549

FORM 6-K

REPORT OF FOREIGN PRIVATE ISSUER
PURSUANT TO RULE 13a-16 OR 15d-16 UNDER
THE SECURITIES EXCHANGE ACT OF 1934

For the month of July 2023

Commission File Number: 001-41426

Nano Labs Ltd

(Exact name of registrant as specified in its charter)

30th Floor, Dikaiyinzuo
No. 29, East Jiefang Road,
Hangzhou, Zhejiang
People's Republic of China
(Address of principal executive office)

Indicate by check mark whether the registrant files or will file annual reports under cover of Form 20-F or Form 40-F.

Form 20-F ☒ Form 40-F ☐

SIGNATURES

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned, thereunto duly authorized.

Nano Labs Ltd

Date: July 31, 2023

By: /s/ Jianping Kong

Name: Jianping Kong

Title: Chairman and Chief Executive Officer

EXHIBIT INDEX

Exhibit No.	Description
Exhibit 99.1	Press Release

Exhibit 99.1

Nano Labs Announces Receipt of Interest-Free Loans from Shareholders

HANGZHOU, China, July 28, 2023 (GLOBAL NEWSWIRE) -- Nano Labs Ltd (Nasdaq: NA) ("we," "the Company," or "Nano Labs"), a leading fabless integrated circuit design company and product solution provider in China, announced today that it has entered into agreements with Mr. Jianping Kong, our chairman and chief executive officer, and Mr. Qifeng Sun, our vice chairman, along with their respective affiliates, who together will provide interest-free loans in the total amount of $10 million, to fund the Company's research and development initiatives directed towards the advancement of ASIC chips, smart-NICs, and vision computing chips. The loans will become due on June 30, 2024.

About Nano Labs Ltd

Nano Labs Ltd is a leading fabless integrated circuit ("IC") design company and product solution provider in China. Nano Labs is committed to the development of high throughput computing ("HTC") chips, high performance computing ("HPC") chips, distributed computing and storage solutions, smart network interface cards ("NICs") vision computing chips and distributed rendering. Nano Labs has built a comprehensive flow processing unit ("FPU") architecture which offers solution that integrates the features of both HTC and HPC. Nano Lab's Cuckoo series are one of the first near-memory HTC chips available in the market with a maximum bandwidth of approximately 2.27 Tbps, as well as one of the first movers of the ASIC-based Grin mining market *. For more information, please visit the Company's website at: https://ir.nano.cn/.

*According to an industry report prepared by Frost & Sullivan.

For investor inquiries, please contact:

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Ascent Investor Relations LLC

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